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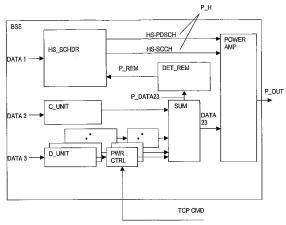
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(54) Title: POWER CONTROL FOR HIGH-SPEED PACKET DATA TRANSMISSION



(57) Abstract: A transmitting unit comprising a first unit (CM_SCHDR) receiving scheduled first data (DATA2, DATA3) for transmission on at least a first channel, a power control unit (PWR_CTRL) for the first channel responsive to a respective closed loop power regulation signal (TCP_CMD), under which at least the transmit power rate of change is limited to a predetermined value per time unit, a packet data scheduler (HS_SCHDR) scheduling second data packets (DATA1), such as HSPDA data, for transmission on at least a second channel at an actual power level (P_H(t)), and a power amplifier (POWER_AMP) amplifying and outputting the scheduled first and second data, whereby the outputted first and second channels are subject to interference from one another, is shown. A possible power (P_POS(t)) is determined at a given instance as the maximum value of either the actual power (P_HS(t-1)) at a previous instance or the possible power determined at a previous instance (P_POS(t-1)), decreasing the maximum value by a predetermined value (d). Moreover, a permitted power (P_PERM(t)) at a given instance as the maximum value of either the actual power of a previous instance (P_HS(t-1)) added with the predetermined value (d) or the determined possible power (P_POS(t)). Finally, the scheduling is performed within these limits.

